

| S.No | Name(s) of the Author  | Title   | National / International | Conf/Jur Name   | Year |
|------|--|---|--------------------------|---|------|
| 1    | G. Chandra Sekhar, P.S. Subramanyam and D. Padmavathi                | Simulation of a Complete Logic Based Protection Scheme for Line and Bus Faults in Six Phase Transmission Line | INT                      | Advances in Electronic and Electric Engineering       | 2014 |
| 2    | Dr.L.Venkata Narasimha Rao, Ramesh.Vaddi                             | Simulation of Cuk Converter Using Incremental Conductance MPPT with Direct Control Method                     | INT                      | IJSETR  | 2014 |
| 3    | JarupulaSomlal, Dr. Venu Goplala Rao Mannam, Narsimha Rao Vutlapalli | FUZZY Logic Based Space Vector PWM Controlled Hybrid Active Power Filter for Power Conditioning               | INT                      | WSEAS TRANSACTIONS on POWER SYSTEMS                   | 2014 |
| 4    | JarupulaSomlal, Dr. Venu Goplala Rao Mannam, Narsimha Rao Vutlapalli | Performance Analysis of Artificial Neural Network Based Shunt Active Power Filter                             | INT                      | International Journal of Applied Engineering Research | 2014 |
| 5    | K.Ramalingeswara Rao, K.S.Srikanth                                   | Improvement of Power Quality Using Fuzzy Logic Controller in Grid Connected Photovoltaic Cell Using UPQC      | INT                      | International Journal of Applied Engineering Research | 2014 |
| 6    | K.S.Srikanth   | A Three Phase Multi Level Converter for Grid Connected PV System  | INT                      | International Journal of Applied Engineering Research | 2014 |
| 7    | M.Kiran Kumar, GRK Murthy, SS Srinivas Addala                        | Open Loop and Closed Loop Performance of Switched Reluctance Motor with Various Converter Topologies          | INT                      | International Journal of Applied Engineering Research | 2014 |
| 8    | Dr.L.V.Narasimha rao   | PSO Technique for Solving the Economic Dispatch Problem Considering the Generator Constraints                 | INT                      | IJAREEIE  | 2014 |
| 9    | Dr. Obbu Chandra Sekhar Dr.K chandrasekhar Dr.G Durga sukumar        | An Enhanced DTC scheme for Induction Machine Control Fed by Seven-Level MPC Voltage Source Inverter           | INT                      | Journal of Electrical Engineering                     | 2014 |