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|----------|-----------------------|-----|---|---|---|--|--|---|--|--|--|--|--|--|--|---|--|--|
|          |                       | CO5 | An ability to analyze & generate experimental skills  | 4 | 4 |  |  | 4 |  |  |  |  |  |  |  | 4 |  |  |
| 17EC2102 | Digital System Design | CO1 | Able to understand the computer system and its sub modules, handling call   | 3 | 3 |  |  | 3 |  |  |  |  |  |  |  |   |  |  |
|          |                       | CO2 | Understand the functionality and design the CPU functional units - control unit, registers, the arithmetic and logic unit, the instruction execution unit, and the interconnections among these components. | 3 | 3 |  |  | 3 |  |  |  |  |  |  |  |   |  |  |
|          |                       | CO3 | Understand, analyse and design different types of I/O transfer techniques.  |   | 3 |  |  | 3 |  |  |  |  |  |  |  |   |  |  |
|          |                       | CO4 | Demonstrate various Pipelining, Understand the design issues of RISC and CISC CPUs and the design issues of pipeline architectures  | 3 | 3 |  |  | 3 |  |  |  |  |  |  |  |   |  |  |

professor incharge academics

HoD-ECM