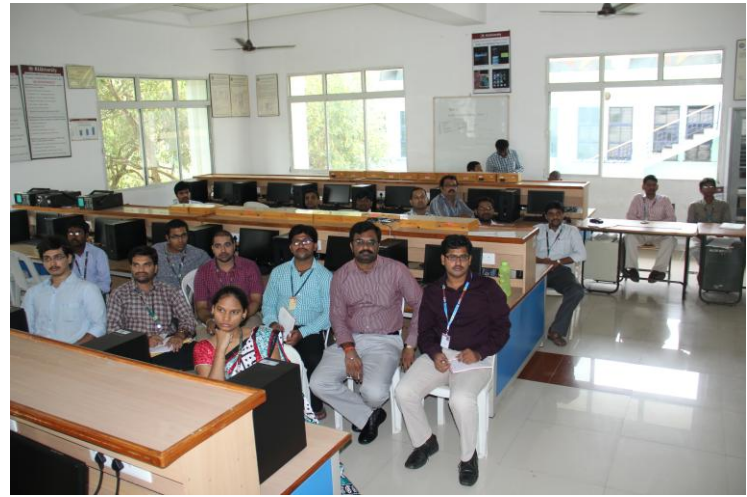# K L UNIVERSITY
DEPARTMENT OF ELECTRONICS AND COMPUTER ENGINEERING

The department of ECM (Embedded Systems & Sensor Networks Research group) along with Academic Staff College, K L University has organized a Faculty Development Program in ELECTRONIC SYSTEMS DESIGN from 22nd June to 25th June 2015 in association with Pantech Pro Labs, Chennai. Forty faculty from ECM, ECE, EEE and CSE have participated in the Faculty Development Program. During the FDP the participants gained the knowledge and hands on experience in two major areas of Electronic Systems Design, i.e., VlSI design and DSP applications using DSP processor TMS 320C6745. The schedule of the program was

22nd & 23rd June 2015   -   VLSI Design.

24th & 25th June 2015   -   DSP Applications using DSP processor TMS 320C6745.

During 22nd and 23rd Fundamentals of FPGA/CPLD were discussed with reference to disparity of FPGA vs CPLD from different vendors. The Xilinx FPGA/CPLD architectures functional blocks were evaluated. Xilinx EDK tool was introduced with the demonstration of HW/SW Co-Simulation for different modulation techniques such as ASK,FSK, BPSK and QPSK. Later the simulated projects were implemented using Spatran -6/3 boards.

During 24th and 25th Texas DSP architecture was introduced. Faculty practiced the creating and debugging of the projects using CCS tool through the examples like LED blinking, switch interface, Waveform generation UART. In the second day the faculty implemented image processing on TMS320C6745 DSP processor using DWT and also implemented FIR Low-Pass and High-Pass filters.

Dr. K.Sreenivasa Ravi   Dr. M.Suman
(Event Co-ordinator)   (HOD-ECM)