

Koneru Lakshmalah Education Foundation

(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

Accredited by NAAC as 'A++' ◆Approved by AICTE ◆ ISO 9001-2015 Certified Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhre Pradesh, INDIA. Phone No. 08645 - 350200; www.klef.ac.in; www.klef.adu.in; www.kleiniversity.ln Admin Off; 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 886 - 3500122, 2576129.

Ref: KLEF/RO/ECE/CIRCULAR

Date: 06-03-2023

CIRCULAR

Sub: Organizing event "Workshop" for the students of Electronics and Communication Engineering, of Vaddeswaram Campus of KLEF – Reg.

This is to inform that the Department of Electronics and Communication Engineering, KLEF, is Organizing a "Workshop on Emerging Trends in VLSI Technology" for the students of Electronics and communication Engineering, Vaddeswaram Campus of KLEF on, 08-03-2023, as details below:

Event Name: "Workshop"

Date:

08-03-2023

Venue:

R-106

All the students of ECE, are invited to attend this program.

Dy-HOD's & Year coordinators are requested to bring this information to the attention of all ECE students and encourage them to participate in this program.

To All ECE Students, All ECE Faculty, Principal. HODIECE

Dr. M. SUMAN

Professor & Head

Department of ECE

K. I. E. F.

Green Fields, Vaddeswaram

Fintur Dist., A.P. PIN: 522 507

Koneru Lakshmaiah Education Foundation



(Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956)

Accredited by NAAC as 'A++' & Approved by AICTE & ISO 21001:2018 Certified Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA. Phone No. +91 8645 - 350 200; www.klef.ac.in; www.klef.edu.in; www.kluniversity.in Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129

A One-Day Workshop On "Emerging Trends in VLSI Technology".

 $\mathbf{B}\mathbf{y}$

Always@VLSI Department Of ECE

Name of the event: Emerging Trends in VLSI Technology

Dates: 08-03-2023

Venue: R106

No. of students participated: 80

Objective of the event:

The objective of the seminar "Emerging Trends in VLSI Technology" is to provide participants with insights into the latest advancements, innovations, and trends shaping the field of Very Large Scale Integration (VLSI) technology. Through presentations, discussions, and expert insights, the seminar aims to explore cutting-edge technologies, emerging design methodologies, and future directions in VLSI, enabling participants to stay informed about the latest developments and opportunities in the rapidly evolving landscape of semiconductor technology.

Description of the event:

The seminar "Emerging Trends in VLSI Technology" offers a comprehensive exploration of the latest advancements, innovations, and trends shaping the field of Very Large Scale Integration (VLSI) technology. Through a series of presentations, discussions, and expert insights, participants will gain valuable insights into the cutting-edge technologies, emerging design methodologies, and future directions in VLSI. Topics covered in the seminar include novel semiconductor devices, advanced fabrication techniques, innovative circuit architectures, and system-level integration approaches. Additionally, discussions will focus on the impact of emerging technologies such as artificial intelligence (AI), Internet of Things (IoT), and edge computing on VLSI design and implementation. Participants will have the

opportunity to engage with industry experts, researchers, and practitioners, gaining firsthand knowledge and perspectives on the latest trends and developments in VLSI technology. Overall, the seminar aims to provide participants with a forward-looking view of the VLSI landscape, empowering them to stay informed and adapt to the rapidly evolving field of semiconductor technology.

Outcome of the event:

The outcome of the seminar "Emerging Trends in VLSI Technology" is participants who are informed and equipped with valuable insights into the latest advancements, innovations, and trends in the field of Very Large-Scale Integration (VLSI) technology. Through presentations, discussions, and expert insights, attendees gain awareness of cutting-edge technologies, emerging design methodologies, and future directions in VLSI. Armed with this knowledge, participants can adapt to the rapidly evolving landscape of semiconductor technology, anticipate future developments, and make informed decisions in their research, development, and design efforts. Additionally, the seminar fosters networking opportunities and collaboration among participants, industry experts, researchers, and practitioners, facilitating knowledge exchange and potential collaborations to further drive innovation and advancements in VLSI technology.

Photos of the event:





Faculty addressing students

Participant's List:

S. NO	STUDENT ID	STUDENT NAME	DEPT	SIGNATURE
1	2100049095	ALETI SANTHOSH	ECE	Saulath
2	2100049084	JALDU MOUNIKA NAGA RAMA SUDHA	ECE	Sudha
3	2100049082	MEDISETTI GUNASRI	ECE	Gwario.
4	2100049081	MALE SAI MANIKANTA	ECE	N. Marikanto
5	2100049080	TALLA VENKATA GOPI ROHITH	ECE	For Brough
6	2100049079	VELAMARTHI ABHINAY ANAND	ECE	Anunla
7	2100049077	VEERAMACHANENI VENKATA NAVEEN	ECE	Now
8	2100049076	YANAMALAMANDA VAMSI	ECE	y. Jansi
9	2100049072	CHEEPURUPALLI BALA SRIDHAR	ECE	Souther
10	2100049071	ADIREDDY JYOTHI BHAVANI SHANKAR	ECE	Someter
11	2100049068	JANGILI SIVA SAI PRAKASH	ECE	Saifrakach
12	2100049065	UPPALA RAVI TEJA	ECE	Raus
13	2100049064	POKALA SURYA AKASH	ECE	Afurt
14	2100049063	PRUDHVI GOPAL VELISALA	ECE	V. Predhu
15	2100049051	SYED ASHRUF	ECE	-Ashung Sq
16	2100049050	MADEM SIVA MASTHAN REDDY	ECE	Rudy
17	2100049047	KORIVI AKHIL	ECE	All town
18	2000049026	SRIPATHI CHANDANA	ECE	Schandan
19	2000049022	KRAPA LOKESH	ECE	Lek O.
20	2000049019	VYSHNAVI VIPPAGUNTA	ECE	1 la Al
21	2000049016	JINKA JITENDRA SAI KARTHIK	ECE	2 1 kothel

22	2000049015	YADALA VENKATA NITHISH KUMAR	ECE	intiss
23	2000049013	GUDURU BALAJI INDUJA	ECE	Troleijes
24	2000049012	RAMALA DHEERAJ	ECE	X. Dheevaj
25	2000049011	KAMMILI LALITH MADHAV	ECE	Love Modar
26	2000049009	VADDI DEEPAK	ECE	Luch
27	2000049007	SUDUNAGUNTA SREE VARSHINI	ECE	Jack is
28	2000049045	CHAPARALA TEJA SRI	ECE	0092
29	2000049041	MELAM VEERA SAI TEJA	ECE	Contegora
30	2000049032	KUCHIPUDI LAKSHMI MANJU	ECE-	Sailey)
31	2000049031	GEDELA YASHWANTH SAIRAM	ECE	1 S. Mange
JENNA.	2000049029	KUNDELLA GANESH YADAV	ECE	Yeshish Yata
32			ECE	ganess
33	2000049028	MYNAMPATI VENKATA SAI SRI		Marsin
34	2000040281	VANTEDDU PRAVEEN REDDY	ECE	N. Vancen.
35	2000040273	GAYATHRI KETINENI	ECE	Gazatte
36	2000040272	APPANI GOPICHANDU	ECE-	Grah Chand
37	2000040270	PODURI ABHISHEK SURYA	ECE	Swyo
38	2000040269	MANOJ KUMAR ROKKAM	ECE	Manoj
39	2000040268	GANDREDDI CHAITANYA NAIDU	ECE	G. Chaitonyo.
40	2000040266	BOJJA SIVA VAISHNAVI	ECE	Valuari
41	2000040261	MALISETTY ROHAN	ECE	W M
42	2000040258	PATHAN HASAN ZAHEERKHAN	ECE	hasen
43	2000040256	NALI BHARGAVA SARASWATHI	ECE	Covaretti.
44	2000040252	KOTA LAKSHMI MANOGNA	ECE	Salatauthi.

45	2000040249	BODDOJU VISHNU VARDHAN	ECE	Juso.
46	2000040245	GURRAMPATI PREETHI REDDY	ECE	Truthi
47	2000040244	VISHNU VARDHAN REDDY	ECE	rolle
48	2000040196	RAJARIKAM KIREETI	ECE	Lixel O.
49	2000040193	KANAMARLAPUDI VENKATA SURYA	ECE	Swya
50	2000040187	VALLAMKONDA SATVIKA	ECE	Stilla

S.Vaily

In charge Always@VLSI Technical Club Mr S. Vamsee Krishna HODECE
Dr M. Suman
Dr. M. SUMAN
Professor & Head
Department of ECE
K L E F
Green Fields, Vaddeswarami
Funtur Dist., A.P. PIN' 522 502