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#### Ref: KLEF/RO/ECE/CIRCULAR

#### Date: 12-12-2021

#### CIRCULAR

Sub: Organizing event "Workshop" for the students of Electronics and Communication Engineering, of Vaddeswaram Campus of KLEF – Reg.

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This is to inform that the Department of Electronics and Communication Engineering, KLEF, is Organizing a "Workshop on Programming with Z board" for the students of Electronics and communication Engineering, Vaddeswaram Campus of KLEF on, 16-12-2021, as details below:

Event Name:	"Workshop"
Date:	16-12-2021
Venue:	R-106

All the students of ECE, are invited to attend this program.

Dy-HOD's & Year coordinators are requested to bring this information to the attention of all ECE students and encourage them to participate in this program.

To All ECE Students, All ECE Faculty, Principal.

Dr M. Suman Dr. M. SUMAN Professor & Head Department of ECE K L E F Green Fields, Vaddeswarani Suntur Dist., A.P. PIN: 522 507



Koneru Lakshmaiah Education Foundation (Category -1, Deemed to be University estd. u/s. 3 of the UGC Act, 1956) Accredited by NAAC as 'A++' & Approved by AICTE & ISO 9001-2015 Certified Campus: Green Fields, Vaddeswaram - 522 302, Guntur District, Andhra Pradesh, INDIA. Phone No. 08645 - 350200; www.klef.ac.in; www.klef.edu.in; www.kluniversity.in Admin Off: 29-36-38, Museum Road, Governorpet, Vijayawada - 520 002. Ph: +91 - 866 - 3500122, 2576129.

## A Three-day Workshop On "Programming with Z board".

By

# Always@VLSI Department Of ECE

Name of the event: Programming with Z board

Dates:16-12-2021

Venue:R106

## No. of students participated: 82

### **Objective of the event:**

The session's main aim is to introduce participants to the Zed board platform, its hardware components, and its applications in VLSI design. Participants learned the basics of Xilinx Vivado, including creating a new project, adding design files, and synthesizing the design. This hands-on session focused on writing and implementing simple Verilog code for LED's and switches on the Zed board. Participants learned how to integrate a soft-core processor (Micro Blaze) into their FPGA design, connecting it to various peripherals. Attendees acquired practical skills in using Xilinx Vivado, Verilog programming, and working with the Zed board platform.Several participants successfully completed their embedded system projects, showcasing their abilities to design and implement real-world applications.

### **Description of the event:**

Workshop on 'Programming with Z board' is a specialized event aimed at providing participants with hands-on experience and in-depth knowledge of programming using the Z board platform. The Z board, likely referring to the Zynq SoC (System on Chip) development boards offered by Xilinx, is a powerful tool for developing embedded systems and applications.

Throughout the workshop, participants can expect to learn various aspects of programming with the Z board, including but not limited to:

- Introduction to Zynq SoC architecture and its components.
- Setting up development environment and tools for Z board programming.
- Understanding the basics of embedded programming using languages such as C/C++ and/or Python.
- Exploring hardware-software co-design methodologies enabled by the Zynq SoC.
- Implementing real-world projects and applications on the Z board.
- Debugging and troubleshooting techniques specific to Zynq-based systems.

The workshop is likely to feature a combination of lectures, hands-on sessions, and demonstrations to ensure participants gain practical experience alongside theoretical knowledge. Experienced instructors or experts in the field are expected to lead the sessions, providing valuable insights and guidance to attendees.

#### **Outcome of the event:**

Attendees had the opportunity to present their projects, demonstrating the skills they had acquired during the workshop. The workshop provided a platform for networking and collaboration among participants, fostering a sense of community within the Always VLSI Club. Feedback from participants was overwhelmingly positive. Attendees appreciated the hands-on approach, well-structured content, and the opportunity to apply what they learned in real projects.Workshop participants are likely to share their newfound knowledge and experiences with their peers and colleagues, further disseminating the valuable information and skills acquired during the workshop.Attendees developed hands-on skills in using Xilinx Vivado, Verilog programming, and working with the Zedboard platform. These skills are invaluable for pursuing careers in FPGA design and related industries.

# Photos of the event:



Faculty explaining the FPGA design flow



Students practicing the design flow



Student mentor giving demonstration on design flow

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